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37. The internal cache is loaded with the base address, offset address, and access rights byte 39. The GDTR address the Global Descriptor Table 41.4,096 43. 4M 45. 30000000H 49.

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Brey, Intel Microprocessors, The, 8th Edition | Pearson

About the Author. Birthplace: Barry B. Brey was born in Allentown, PA in 1947 (baby boomer, I guess). Childhood: I lived in Emmaus, PA, a small town (15,000 pop.) located at the edge of Allentown. While in school, I worked for my grandfather, a plumber, which gave me a taste for things flowing through conduits.

About the publications of BARRY B. BREY

The INTEL Microprocessors: 8086/8088, 80186/80188, 80286, 80386, 80486, Pentium, Pentium Pro Processor, Pentium II, Pentium III, Pentium 4, and Core2 with 64-bit Extensions, 8e provides a comprehensive view of programming and interfacing of the Intel family of Microprocessors from the 8088 through the latest Pentium 4 and Core2 microprocessors.

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