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Mashiko, K. Maegawa, S. and Inuishi, M. 2001. Feasibility of 0.18 μm SOI CMOS technology using hybrid trench isolation with high resistivity substrate for embedded RF/analog applications. IEEE ...

The Design of CMOS Radio-Frequency Integrated Circuits

In the past, much emphasis was placed on the cleanroom environment state-of-the-art ULSI fabrication facilities are usually ... the critical feature sizes for state-of-the-art CMOS, bipolar, and ...

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